

# **SSC7002KGS8**

# N-Channel Small Switching MOSFET with ESD Protection

#### > Features

V <sub>DS</sub>	V <sub>GS</sub>	R <sub>DS(ON)</sub> Typ.	I <sub>D</sub>	ESD
60V	±20V	2Ω@10V	0.3A	1kV
000	<u> </u>	3Ω@4V5	0.57	INV

# Description

This device is an N-Channel enhancement mode MOSFET, with low on-resistance, fast switching speed and low threshold voltage, it is ideal for portable equipment.

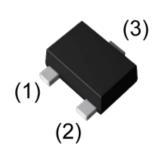
# Applications

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers
- Display, Memories, Transistors, etc.
- Battery Operated System
- Solid-State Relays

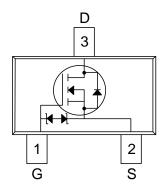
# Ordering Information

Device	Package	Shipping
SSC7002KGS8	SOT-523	3000/Reel

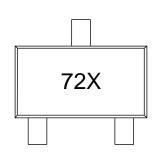
# Pin configuration



**SOT-523** 



Pin Configuration (Top View)



**Marking** 



# ➤ Absolute Maximum Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
V <sub>DSS</sub>	Drain-to-Source Voltage	60	V
V <sub>GSS</sub>	Gate-to-Source Voltage	±20	V
I <sub>D</sub>	Continuous Drain Current <sup>a</sup>	0.3	Α
I <sub>DM</sub>	Pulsed Drain Current <sup>b</sup>	0.9	Α
P <sub>D</sub>	Power Dissipation <sup>c</sup>	0.6	W
ТJ	Operation junction temperature	-55~150	$^{\circ}$
T <sub>STG</sub>	Storage temperature range -55~150		$^{\circ}$

# ➤ Thermal Resistance Ratings (T<sub>A</sub>=25°C unless otherwise noted)

Symbol	Parameter	Maximum	Unit
ReJA	Junction-to-Ambient Thermal Resistance a	200	°C/W

### Note:

- a. The value of R<sub>θJA</sub> is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz.copper, in a still air environment with T<sub>A</sub>=25 °C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

SSC-V1.1 www.sscsemi.com Analog Future



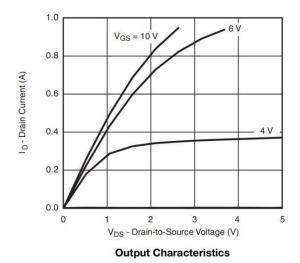
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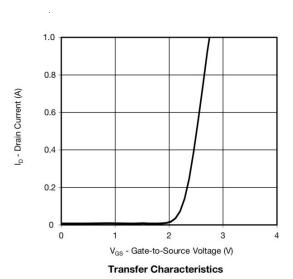
# $\succ$ Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

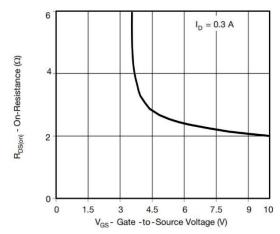
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	60			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250uA$	1	1.5	2.1	V
Drain-Source On-Resistance	D	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.2A	0.2A 2 6	6		
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 0.1A		3	8	Ω
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V			1	μA
Gate-Source Leak Current	Igss	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±10	μA
Transconductance	G <sub>FS</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.2A		0.08		s
Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 0.2A		0.7	1.3	V
Input Capacitance	Ciss	V 00V V 0V		30		
Output Capacitance	Coss	$V_{DS} = 30V, V_{GS} = 0V,$		12		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>	f = 1MHz		4.8		
Turn-on Delay Time	T <sub>D(ON)</sub>			4.6		
Rise Time	Tr	$V_{GS} = 10V, R_L = 60\Omega$		4.1		
Turn-off Delay Time	T <sub>D(OFF)</sub>	$V_{DS} = 20V, R_G = 20\Omega$		24		ns
Fall Time	Tf			18		
Total Gate Charge	Q <sub>G</sub>	101/11/		0.4		
Gate to Source Charge	Q <sub>G</sub> s	$V_{GS} = 10V, V_{DS} = 15V,$		0.1		nC
Gate to Drain Charge	Q <sub>GD</sub>	I <sub>D</sub> = 0.2A		0.11		

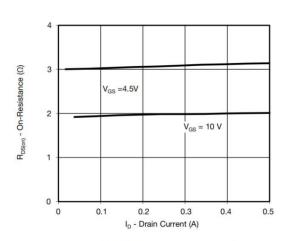


# ➤ Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise noted)



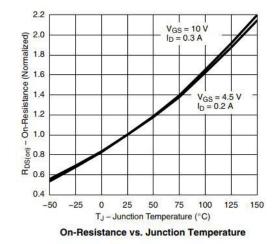


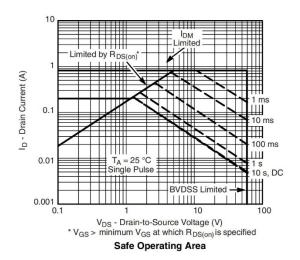




On-Resistance vs. Gate-to-Source Voltage

On-Resistance vs. Drain Current

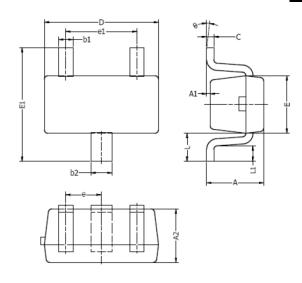


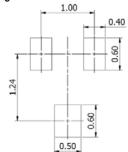




# Package Information

# **SOT-523**





DIM	MILLIMETERS		INCHES		
	MIN	MAX	MIN	MAX	
Α	0.70	0.90	0.028	0.035	
A1	0.00	0.10	0.000	0.004	
A2	0.70	0.80	0.028	0.031	
b1	0.15	0.25	0.006	0.010	
b2	0.25	0.35	0.010	0.014	
С	0.10	0.20	0.004	0.008	
D	1.50	1.70	0.059	0.067	
E	0.70	0.90	0.028	0.035	
E1	1.45	1.75	0.057	0.069	
е	0.50 TYP.		0.020	TYP.	
e1	0.90	1.10	0.035	0.043	
L	0.40 REF.		0.016	REF.	
L1	0.10	0.30	0.004	0.012	
θ	<b>0</b> °	8°	<b>0</b> °	8°	

- NOTES:

  1. Above package outline conforms to JEITA EAIJ ED-7500A SC-75A.

  2. Dimensions are exclusive of Burrs, Mold Flash & Tie Bar extrusions.



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